

FIG. 1

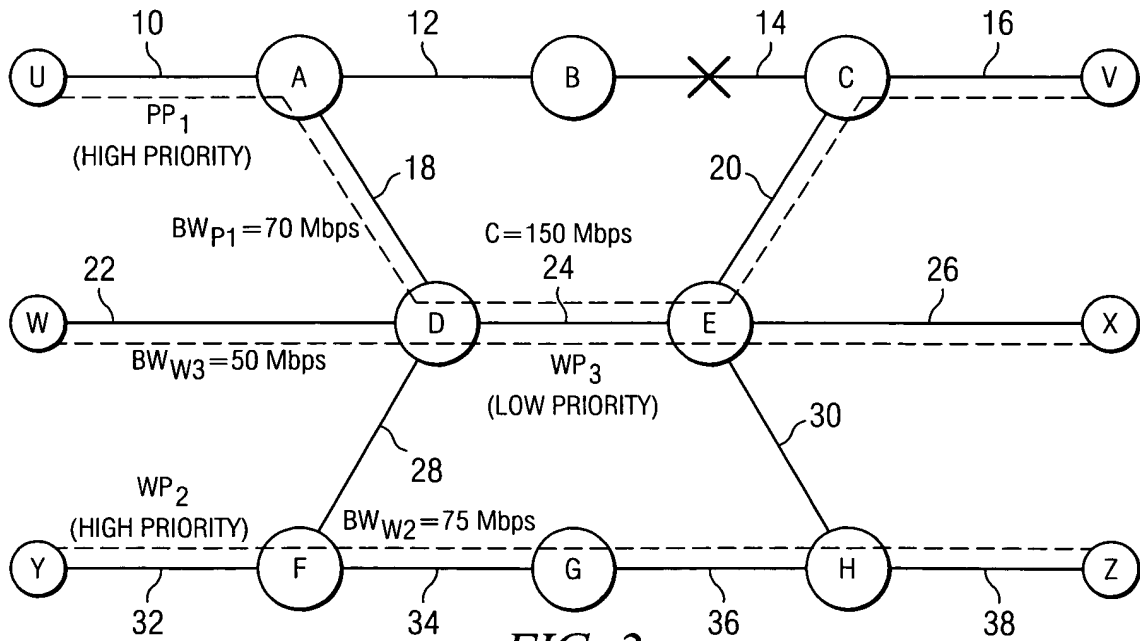
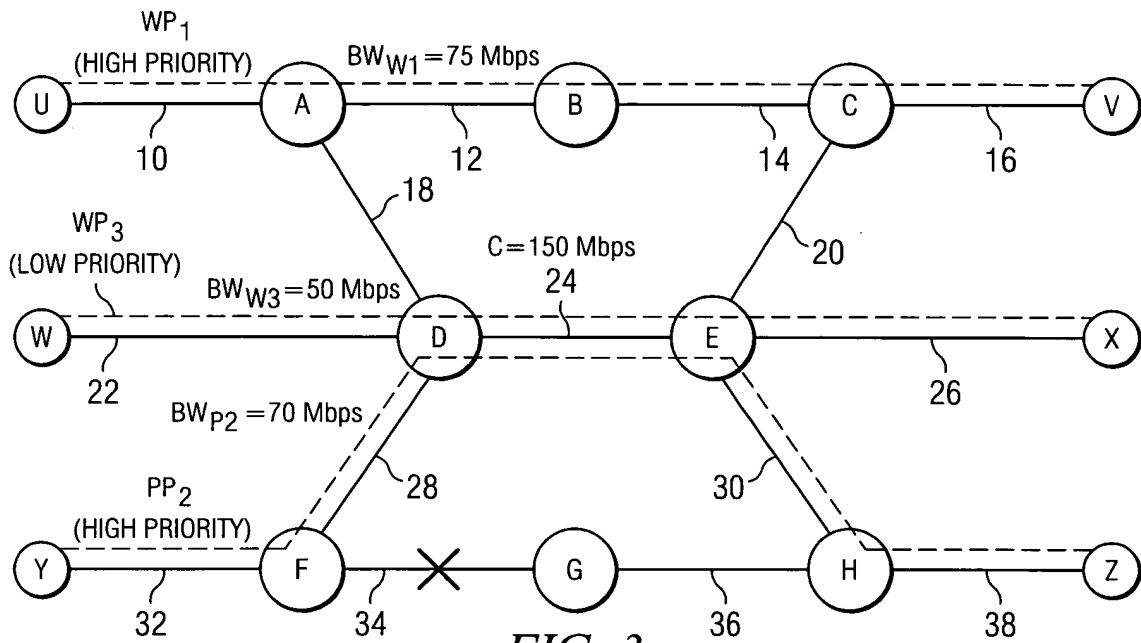
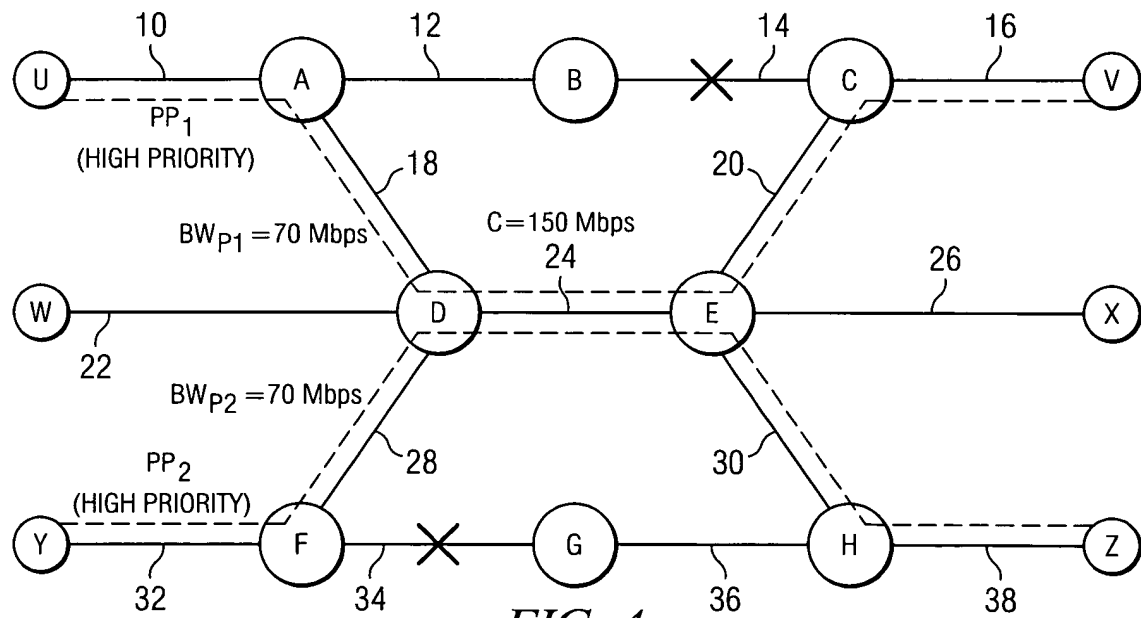


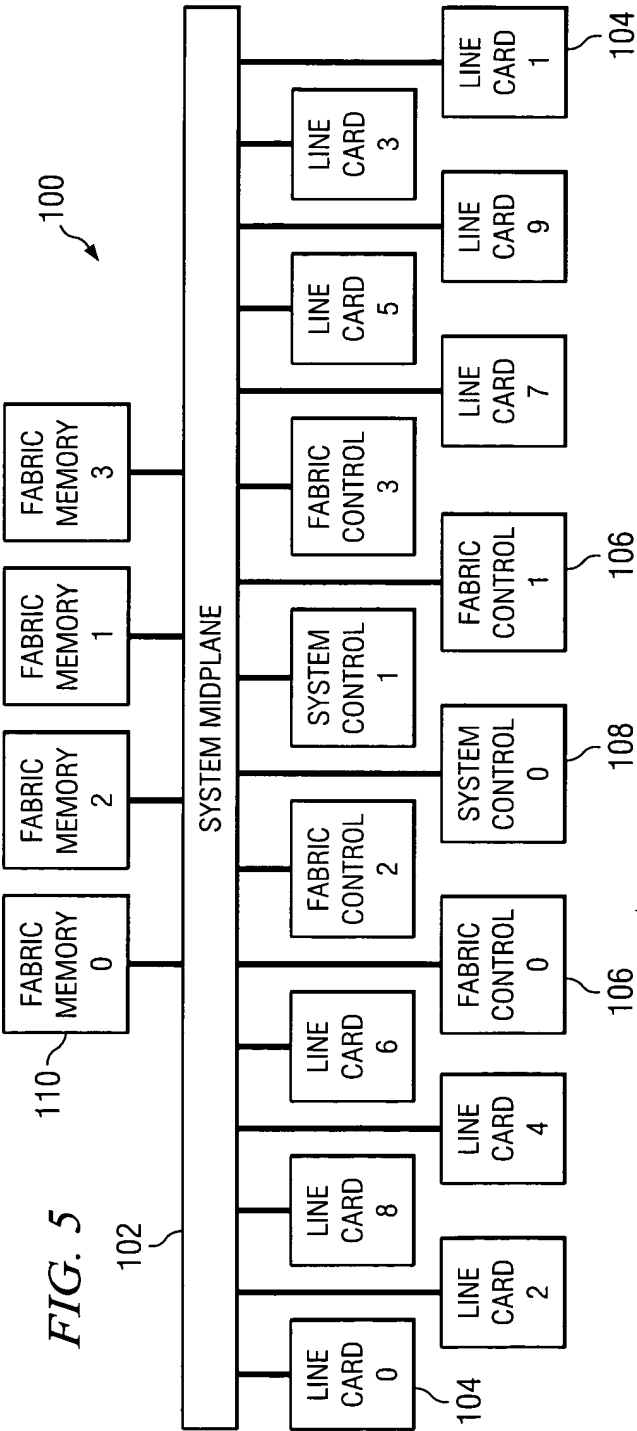
FIG. 2

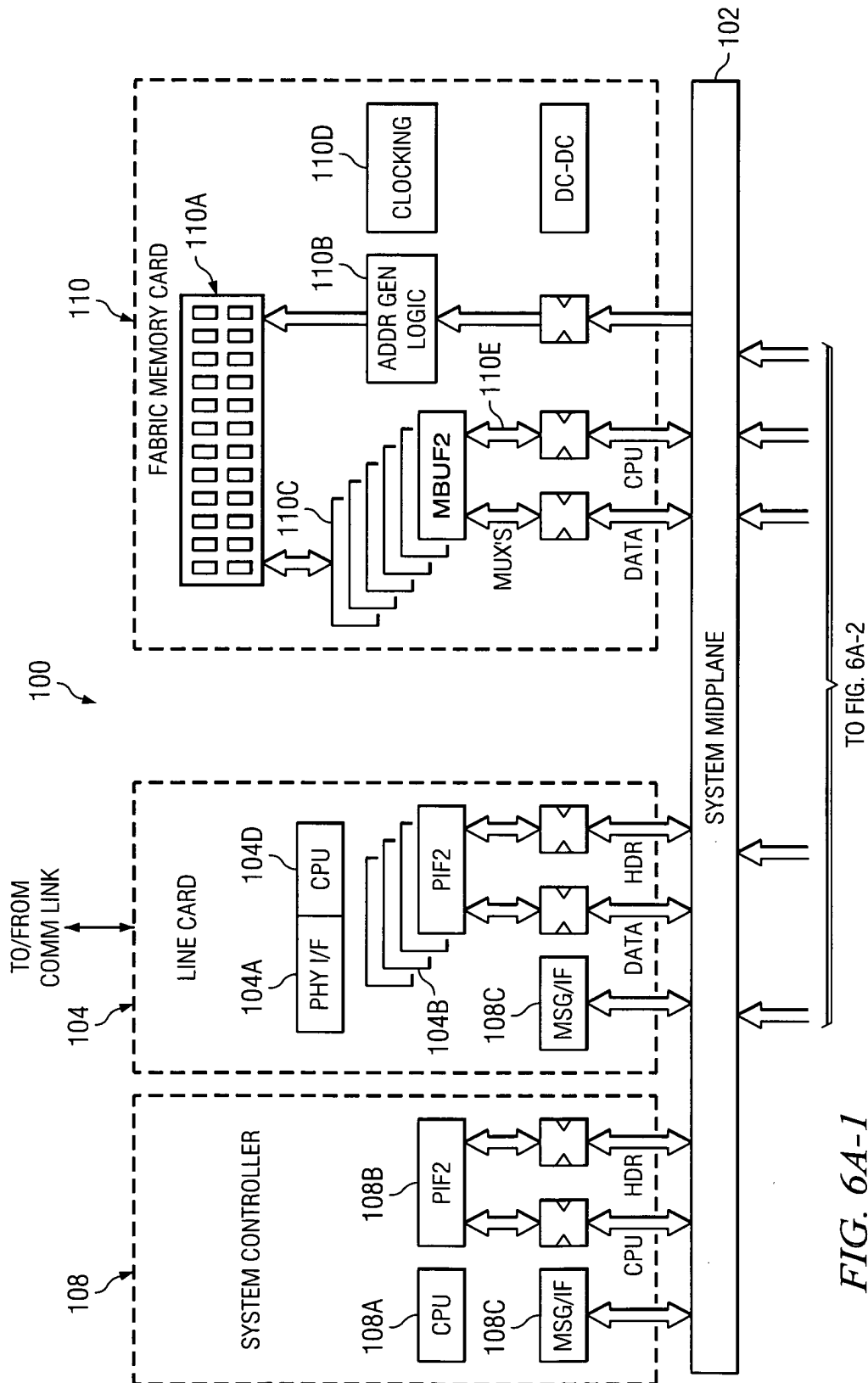


*FIG. 3*

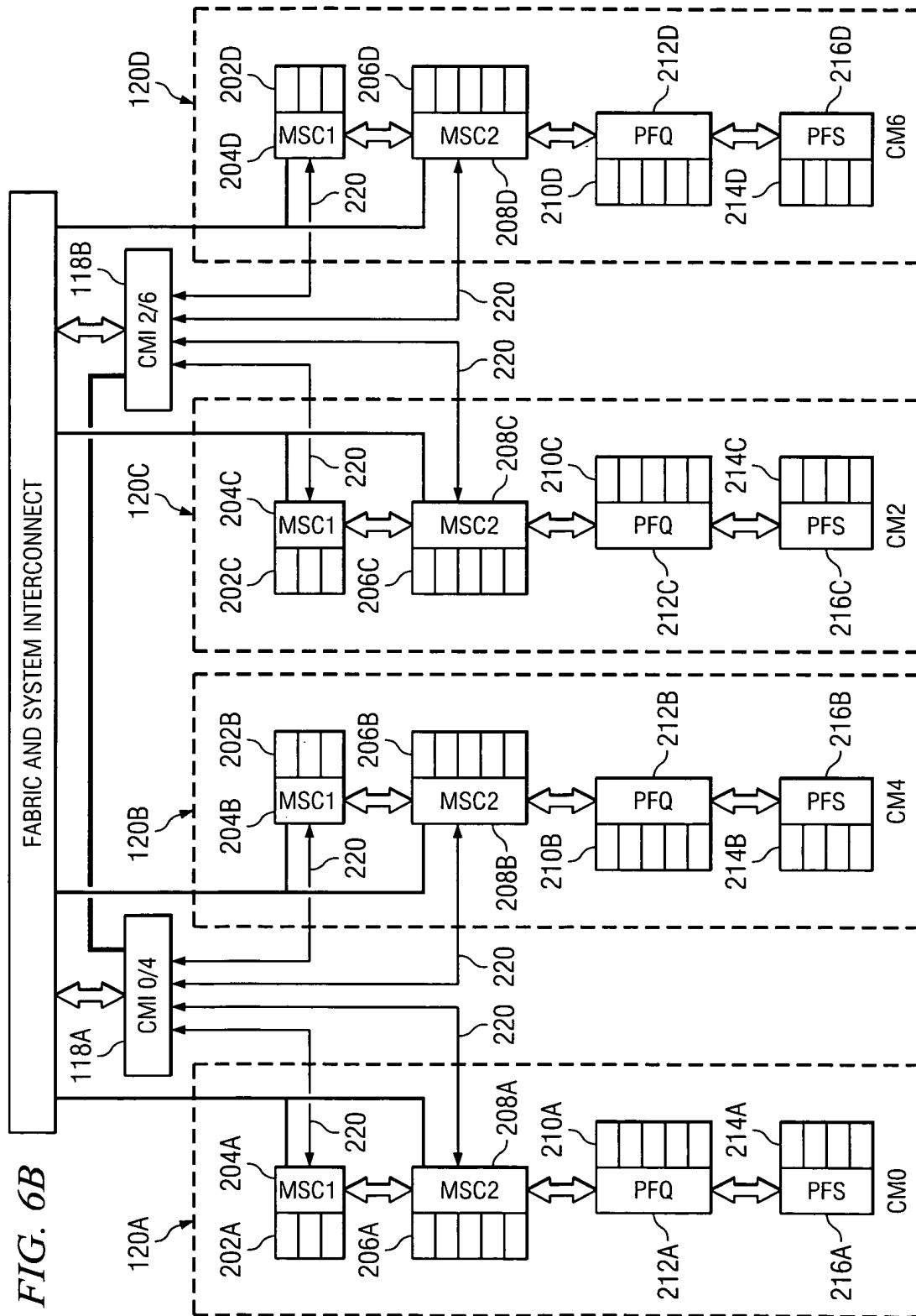


*FIG. 4*









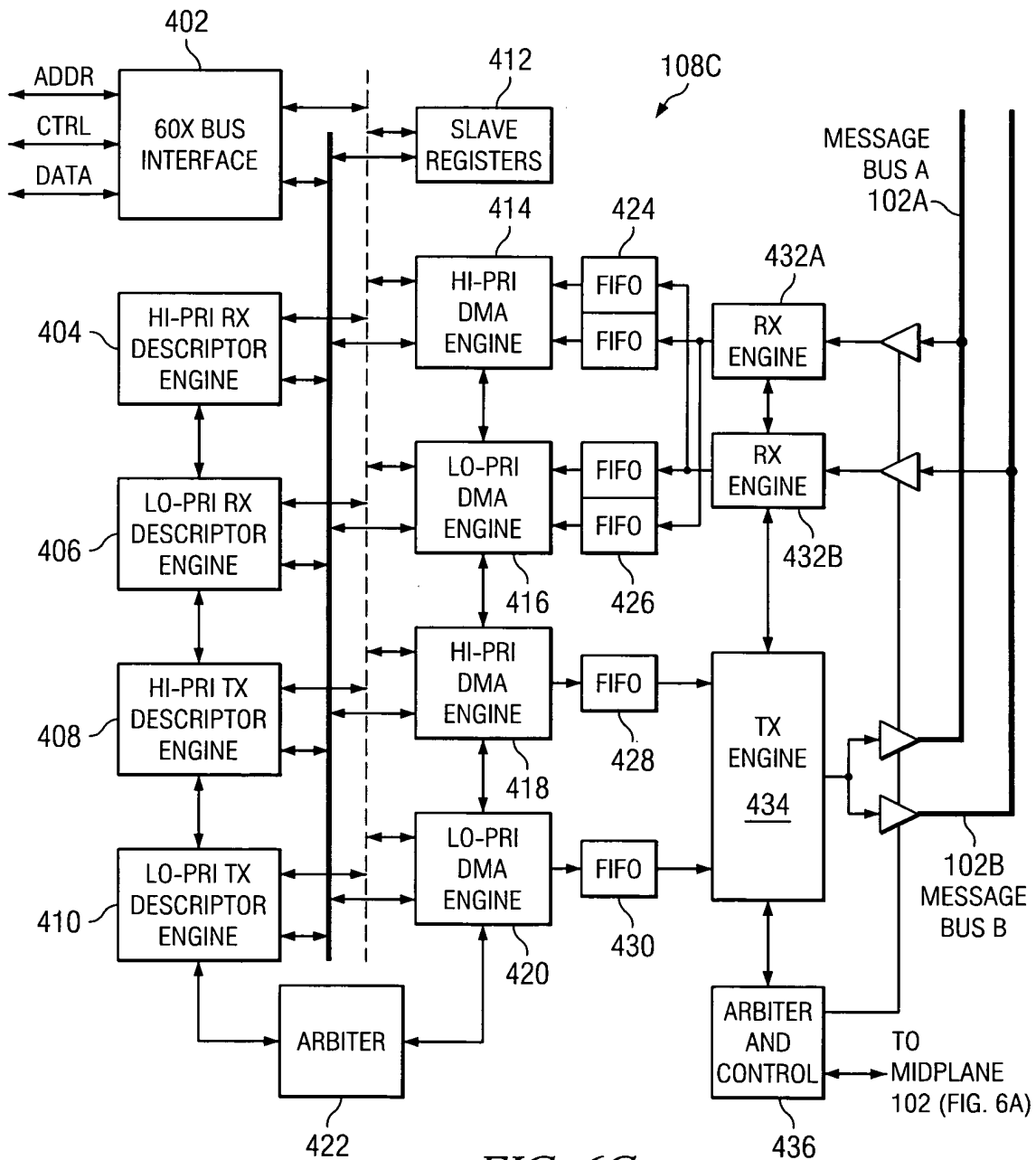


FIG. 6C

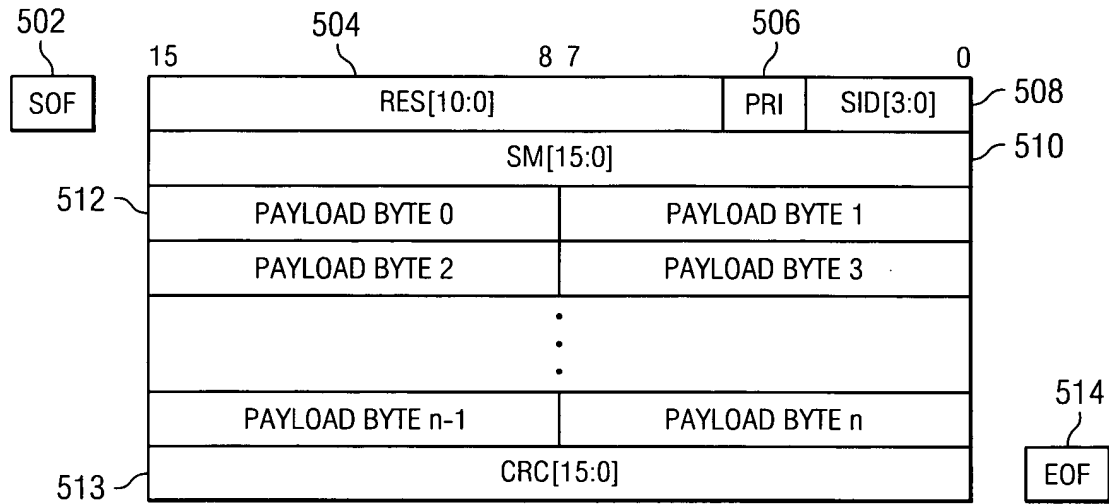


FIG. 6D

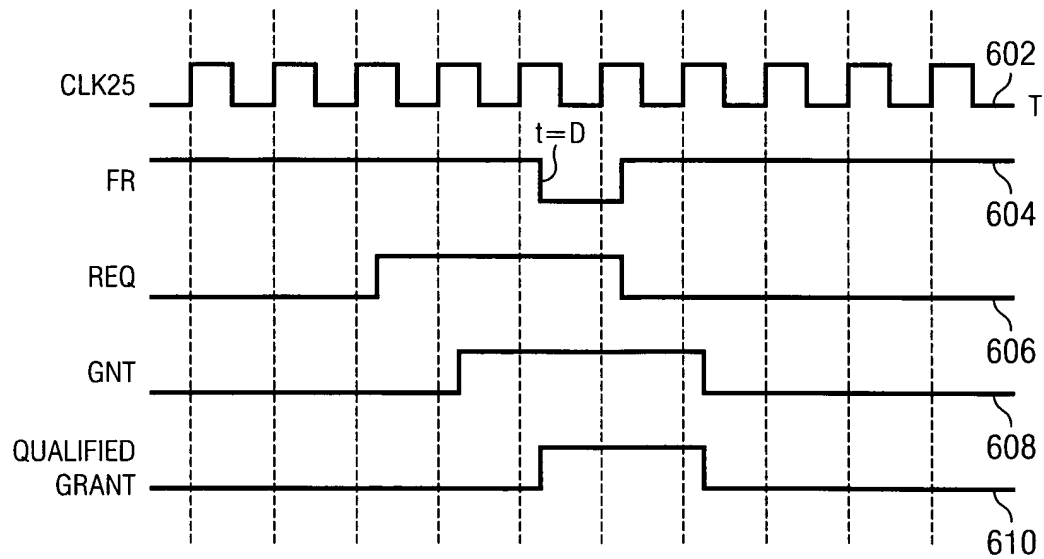


FIG. 6E



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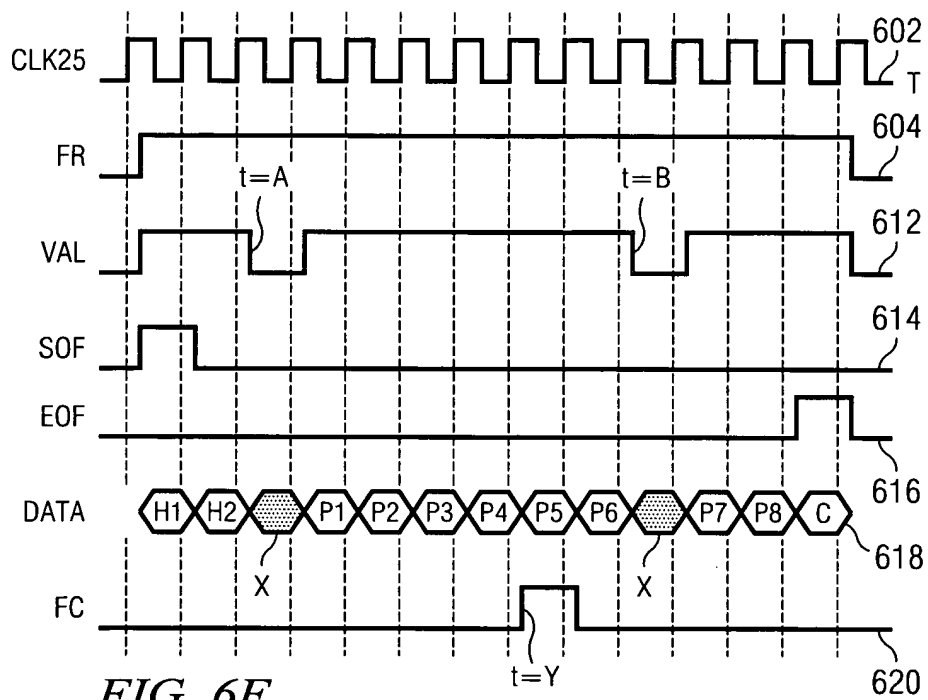


FIG. 6F

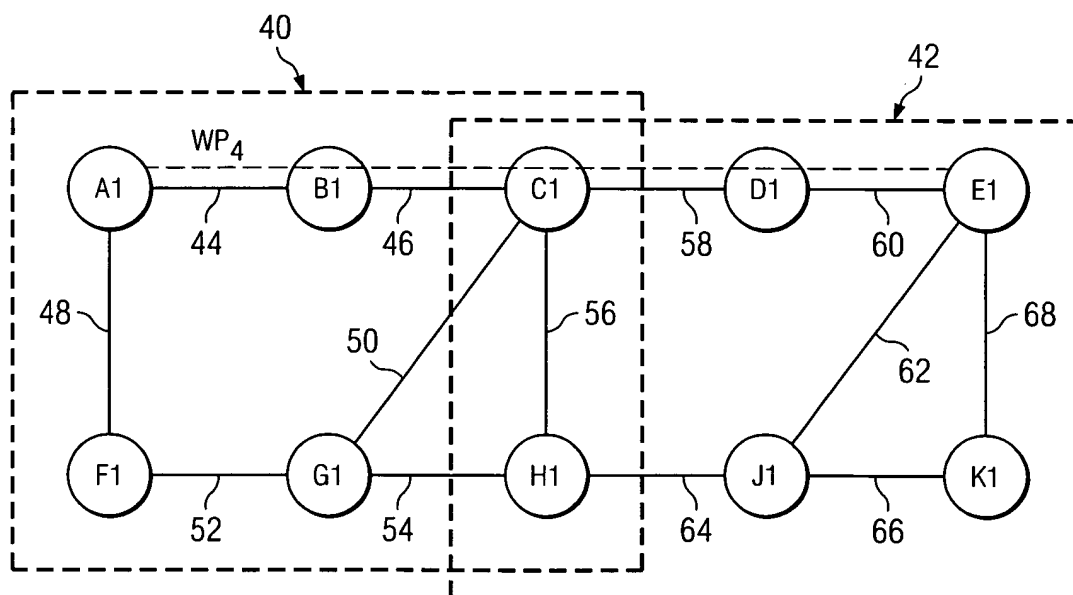
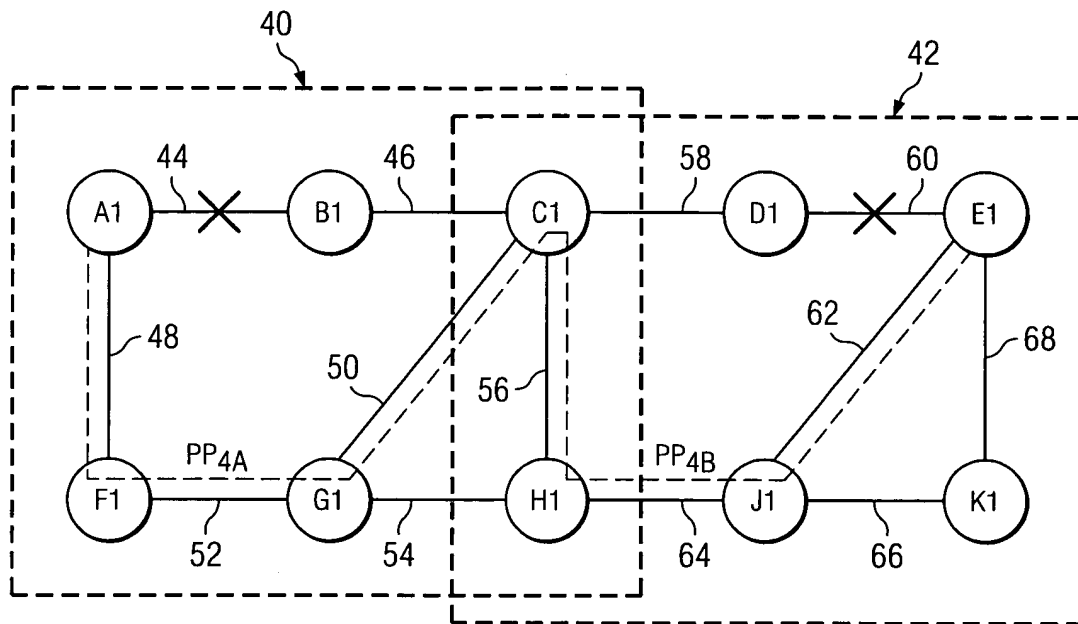
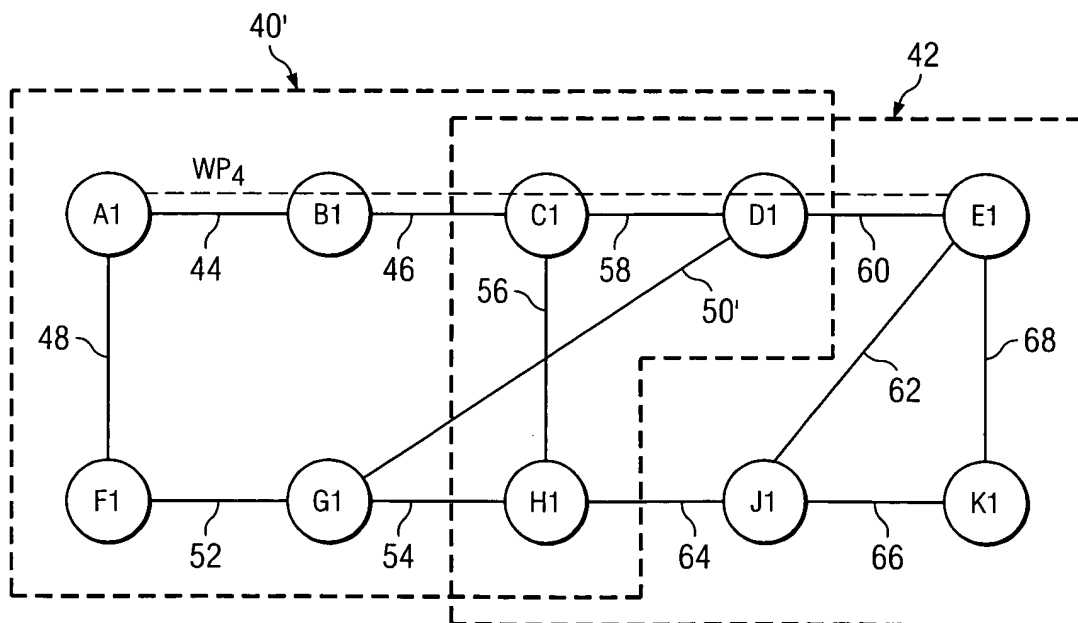


FIG. 7



*FIG. 8*



*FIG. 9*

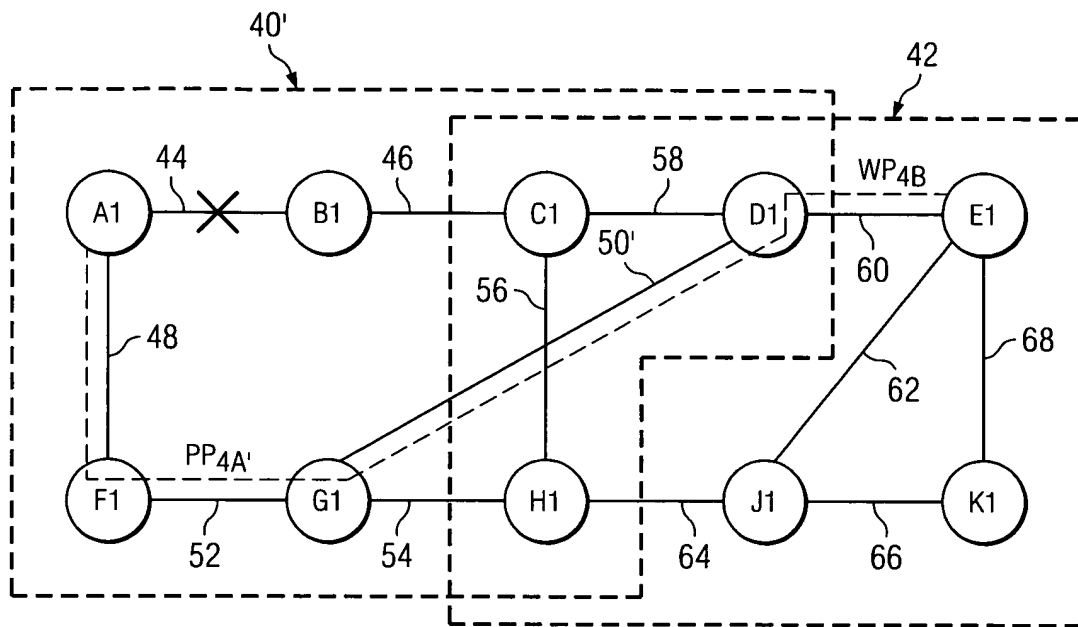


FIG. 10

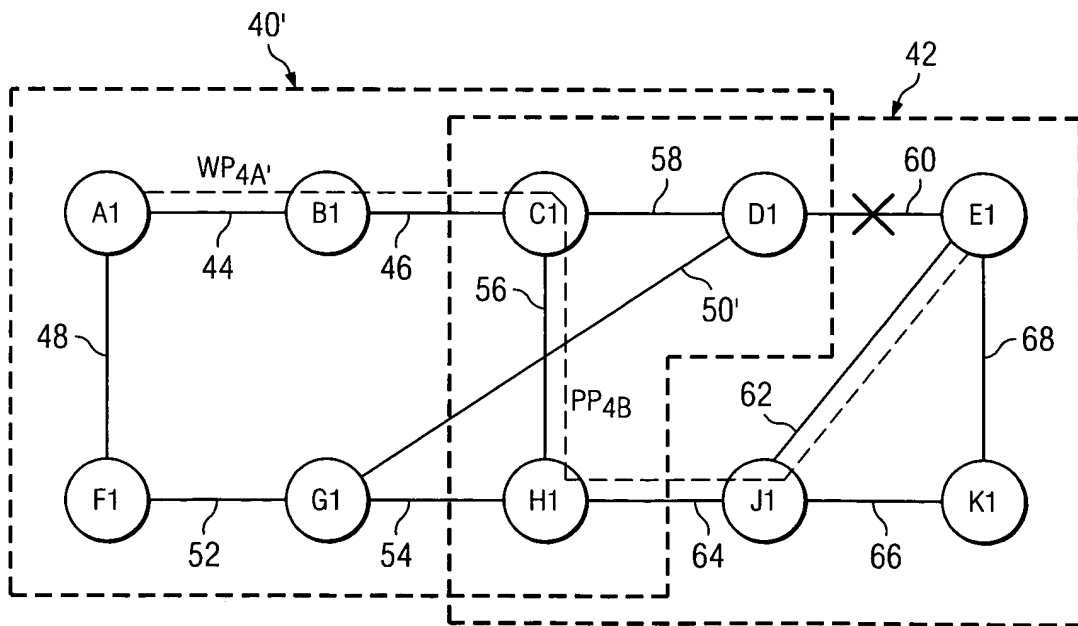


FIG. 11

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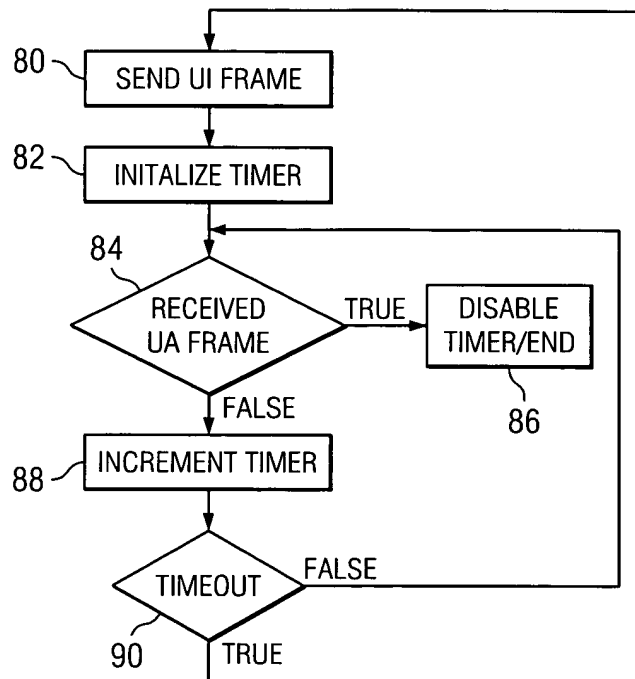


FIG. 12

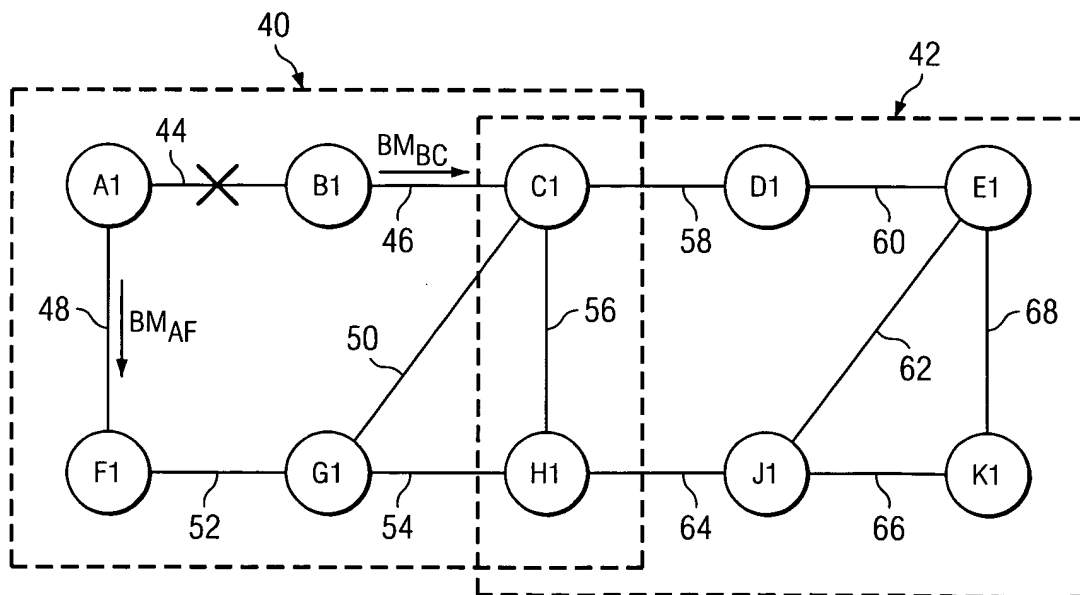


FIG. 13A

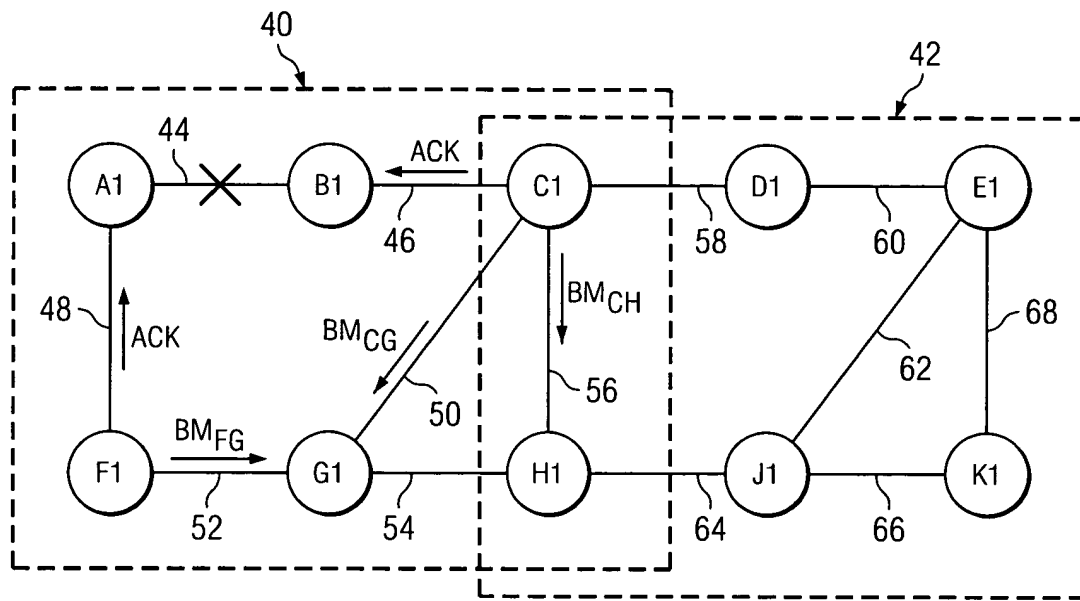


FIG. 13B

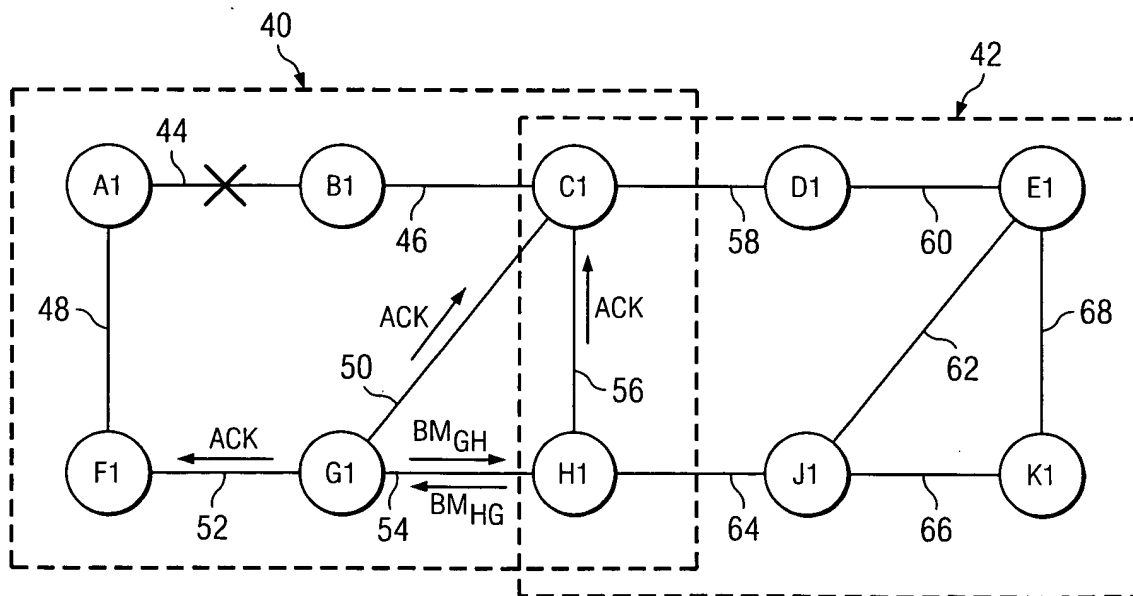
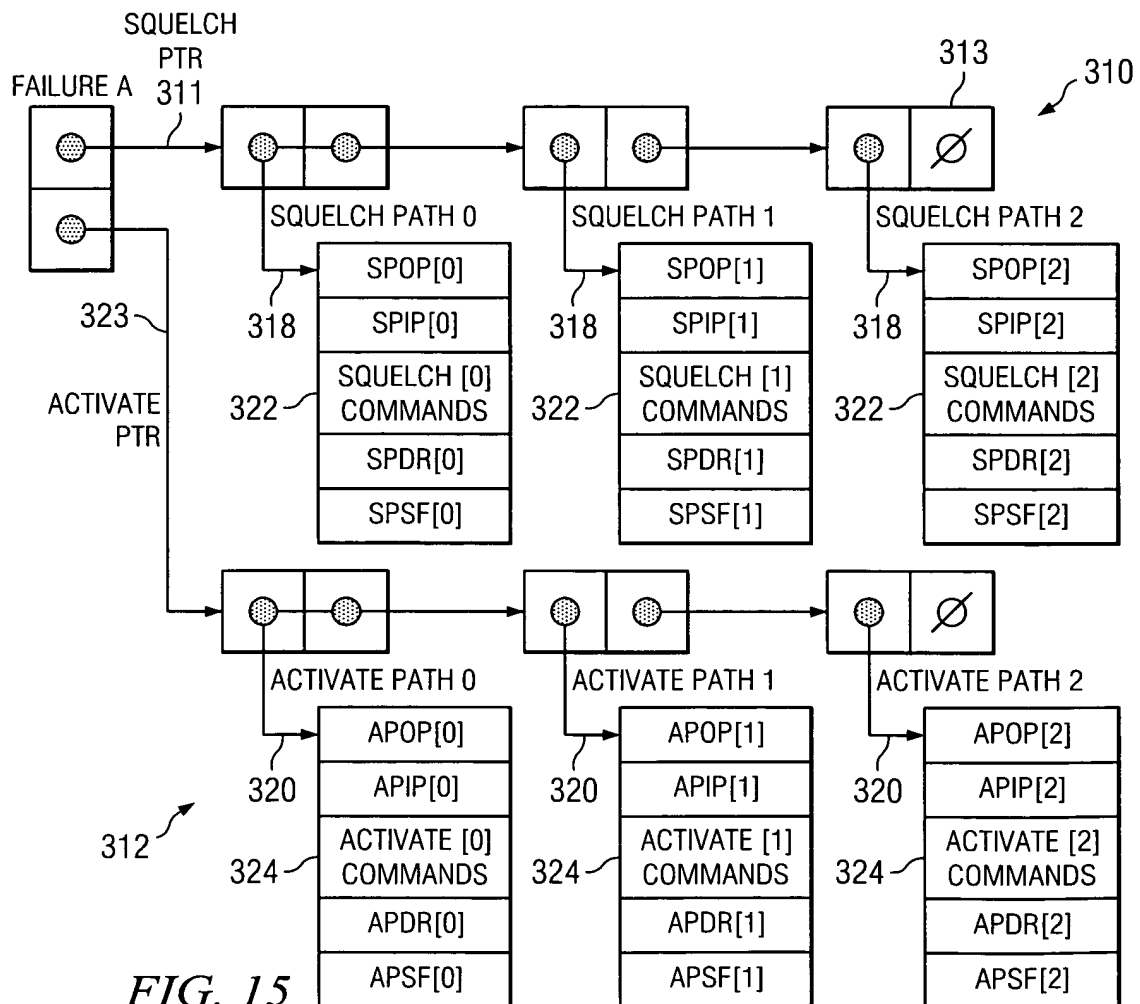
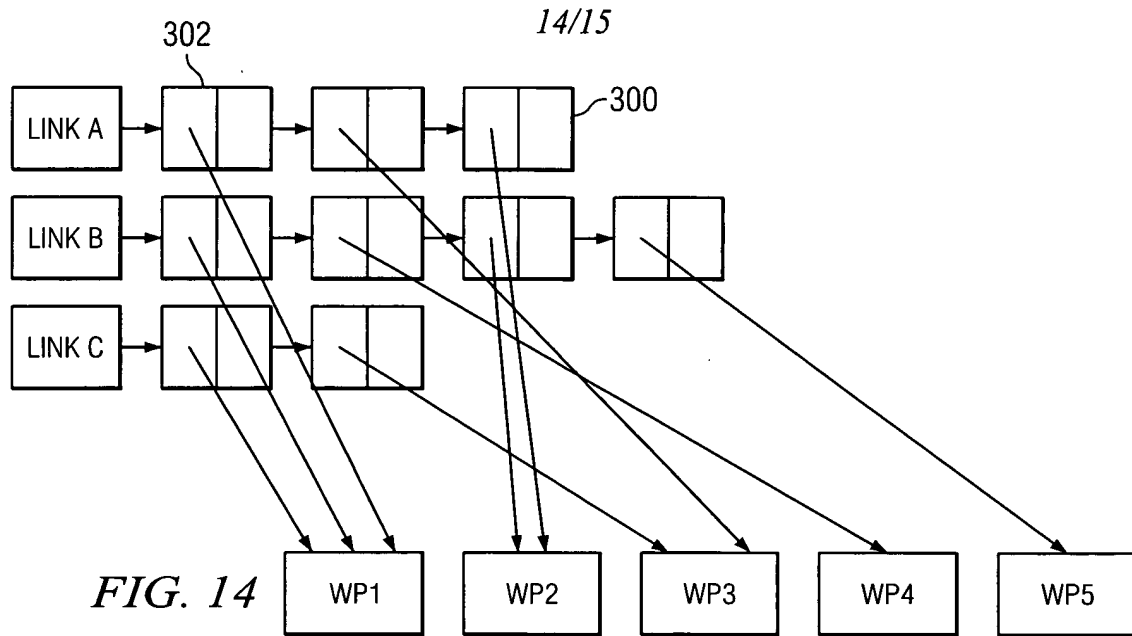
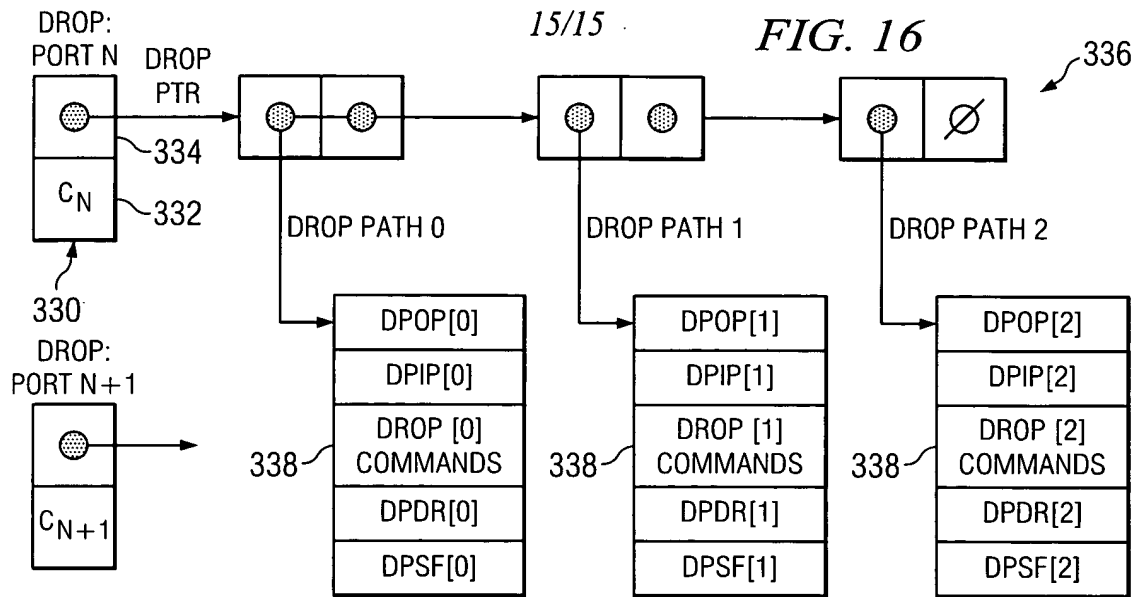


FIG. 13C



*FIG. 17*

0x000	SPARE [15:0]	PORT 0 OUTPUT CAPACITY [15:0]	352
0x004	PORT 0 DROP POINTER [21:0]		
0x008	SPARE [15:0]	PORT 1 OUTPUT CAPACITY [15:0]	350
0x00C	PORT 1 DROP POINTER [21:0]		
0x010	SPARE [15:0]	PORT 2 OUTPUT CAPACITY [15:0]	
0x014	PORT 2 DROP POINTER [21:0]		
0x018	SPARE [15:0]	PORT 3 OUTPUT CAPACITY [15:0]	
0x01C	PORT 3 DROP POINTER [21:0]		
⋮	⋮	⋮	
0x1E0	SPARE [15:0]	PORT 124 OUTPUT CAPACITY [15:0]	
0x1E4	PORT 124 DROP POINTER [21:0]		
0x1E8	SPARE [15:0]	PORT 125 OUTPUT CAPACITY [15:0]	
0x1EC	PORT 125 DROP POINTER [21:0]		
0x1F0	SPARE [15:0]	PORT 126 OUTPUT CAPACITY [15:0]	
0x1F4	PORT 126 DROP POINTER [21:0]		
0x1F8	SPARE [15:0]	PORT 127 OUTPUT CAPACITY [15:0]	
0x1FC	PORT 127 DROP POINTER [21:0]		